Your search matched **3** of **938665** documents.

A maximum of **3** results are displayed, **25** to a page, sorted by **Relevance** in **descending** order.
You may refine your search by editing the current search expression or entering a new one the text box.
Then click **Search Again.**

timestamp-based <sentence>cache<sentence> scheme

Search Again

Results:
Journal or Magazine = **JNL** Conference = **CNF** Standard = **STD**

1 **A compiler-directed cache coherence scheme with improved intertask locality**
Choi, L.; Pen-Chung Yew;
Supercomputing '94. Proceedings , 14-18 Nov 1994
Page(s): 773 -782

[Abstract] [PDF Full-Text (748 KB)] **IEEE CNF**

2 **A timestamp-based selective invalidation scheme for multiprocessor cache coherence**
Xin Yuan; Melhem, R.; Gupta, R.;
Parallel Processing, 1996., Proceedings of the 1996 International Conference on , Volume: 3 , 12-16 Aug 1996
Page(s): 114 -121 vol.3

[Abstract] [PDF Full-Text (792 KB)] **IEEE CNF**

3 **Design and analysis of a scalable cache coherence scheme based on clocks and timestamps**
Min, S.L.; Baer, J.-L.;
Parallel and Distributed Systems, IEEE Transactions on , Volume: 3 Issue: 1 , Jan 1992
Page(s): 25 -44

[Abstract] [PDF Full-Text (1560 KB)] **IEEE JNL**

Your search matched **[0]** of **[938665]** documents.

You may refine your search by editing the current search expression or entering
a new one the text box. Then click search Again.

parallel <near/2>parameter<near/2>space

Search Again

OR

Use your browser's back button to return to your original search page.

Results:

No documents matched your query.

WEST

End of Result Set

☐ Generate Collection Print

L2: Entry 7 of 7 File: USPT Jul 9, 1996

DOCUMENT-IDENTIFIER: US 5535366 A
TITLE: Method of and circuit arrangement for freeing communications resources, particularly for use by a switching element

Current US Original Classification (1):
711/159

Current US Cross Reference Classification (1):
711/151

Current US Cross Reference Classification (2):
711/158

Current US Cross Reference Classification (3):
711/160

Other Reference Publication (5):
S-L Min et al; "A Timestamp-based Cache Coherence Scheme" Proc. 1989 Intern'l Conf. on Parallel Process'g; vol. 1, pp. I23-I32; 8 Aug. '89.

L25	l4 and (((dasd or disk or (storage adj2 device)) with cach$3) with (map$1 or mapping))	48	L25
L24	l4 and (((dasd or disk or (storage adj2 device)) with cach$3) same (map$1 or mapping))	82	L24
L23	l18 and (database or data-base or (data adj2 base))	0	L23
L22	L20 and (time or time-stamp$3 or timestamp$3 or (time adj2 stamp$3))	1	L22
L21	L20 and (time or time-stamp$3 or timestamp$3)	1	L21
L20	5418921.pn.	1	L20
L19	L18 and database	0	L19
L18	4603380.pn.	1	L18
L17	L16 and l15	1	L17
L16	L13 and (map$1 or mapping)	2	L16
L15	L13 and (lru or (least adj2 recently adj2 used))	3	L15
L14	L13 and (timestamp$3 or time-stamp$3 or (time adj2 stamp$3))	0	L14
L13	L4 and (dasd with database)	6	L13
L12	L11 and (dasd with database)	6	L12
L11	((711/113)!.CCLS.)	541	L11
L10	L9 and database	0	L10
L9	L4 and (((dasd) same cach$3) same (time-stamp$3 or (time adj2 stamp$3)))	2	L9
L8	L4 and (((dasd) with cach$3) same (time-stamp$3 or (time adj2 stamp$3)))	2	L8
L7	L4 and (((dasd) with cach$3) with tim$3)	34	L7
L6	L4 and (((dasd or storage) with cach$3) with tim$3)	194	L6
L5	L4 and (((dasd or storage) with cach$3) same tim$3)	305	L5
L4	L3 or ((711/108)!.CCLS.)	2230	L4
L3	L2 or ((711/113)!.CCLS.)	1973	L3
L2	L1 or ((711/202)!.CCLS.)	1458	L2
L1	((711/167)!.CCLS.)	724	L1

END OF SEARCH HISTORY

WEST

Freeform Search

Database:
- US Patents Full-Text Database
- US Pre-Grant Publication Full-Text Database
- JPO Abstracts Database
- EPO Abstracts Database
- Derwent World Patents Index
- IBM Technical Disclosure Bulletins

Term: 117 and ((request$3) adj2 (queue$3 or buffer$3))

Display: 10 **Documents in Display Format:** TI **Starting with Number** 1

Generate: ○ **Hit List** ◉ **Hit Count** ○ **Side by Side** ○ **Image**

Search | Clear | Help | Logout | Interrupt

Main Menu | Show S Numbers | Edit S Numbers | Preferences | Cases

Search History

DATE: Thursday, May 08, 2003 Printable Copy Create Case

Set Name side by side	Query	Hit Count	Set Name result set
	DB=USPT; PLUR=YES; OP=OR		
L19	l17 and ((request$3) adj2 (queue$3 or buffer$3))	9	L19
L18	L15 and ((queue$3 or buffer$3) same (lock$3 with (concurrent$2 or simultaneous$2)))	1	L18
L17	L15 and (lock$3 with (concurrent$2 or simultaneous$2))	30	L17
L16	L15 and ((concurrent$2 or simultaneous$2) same ((request$3) adj2 (queue$3 or buffer$3)))	17	L16
L15	((711/144 |711/145)!.CCLS.)	690	L15
L14	(((711/144,145)!.CCLS.)) and ((concurrent$2 or simultaneous$2) same ((request$3) adj2 (queue$3 or buffer$3)))	0	L14
L13	((711/168)!.CCLS.) and ((concurrent$2 or simultaneous$2) same ((request$3) adj2 (queue$3 or buffer$3)))	11	L13
L12	(((711/168)!.CCLS.)) and ((concurrent$2 or simultaneous$2) same ((request$3) adj2 (queue$3 or buffer$3)))	11	L12
L11	((711/168)!.CCLS.) and ((request$3) adj2 (queue$3 or buffer$3))	44	L11
L10	L7 and ((request$3) adj2 (queue$3 or buffer$3))	73	L10
L9	L7 and ((access$3 or request$3) adj2 (queue$3 or buffer$3))	96	L9
L8	L7 and (request$3 adj2 buffer$3)	25	L8
L7	((711/146)!.CCLS.)	419	L7
L6	L5 and ((request$3 or access$3) adj2 (queue or buffer$3))	0	L6
L5	5806085.pn. or 6449695.pn. or 6532521.pn. or 6532490.pn.	4	L5
L4	L2 and ((cach$3 or coherenc$3) same((data or datum or line or entry) with (expir$5 or timeout or time-out or timing-out)))	39	L4
L3	L2 and ((data or datum or line or entry) with (expir$5 or timeout or time-out or timing-out))	114	L3
L2	((711/133 |711/134 |711/135 |711/136 |711/141 |711/154 |711/159 |711/160 |711/167)!.CCLS.)	2725	L2
L1	((711/$)!.CCLS.)	14297	L1

END OF SEARCH HISTORY